



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Robert B. MAGILL et al.

Appl. No: 10/606,753

Confirmation No: 5555

Filed: June 27, 2003

For: APPARATUS AND METHOD TO  
SWITCH PACKETS USING A  
SWITCH FABRIC WITH MEMORY

Art Unit: 2661

Examiner: Unassigned

Atty. Docket No: 32172-188433

Customer No:

**26694**

PATENT TRADEMARK OFFICE

**Information Disclosure Statement**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted under 37 C.F.R. § 1.97 within the time specified under 37 C.F.R. § 1.97(b).

In order to comply with the duty of disclosure under 37 C.F.R. § 1.56, the U.S. Patent and Trademark Office is notified of the documents which are listed on the attached Form PTO-1449 and which the Examiner may deem relevant to patentability of the claims of the above-identified application. One copy of each of the listed documents is submitted herewith.

The relevance of documents A1, A2 and A4 are mentioned in the attached specification.

The present Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits, and therefore no Statement Under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In view of the above, no further translation or statement of relevance is required, and as all requirements of 37 C.F.R. § 1.97 and all official guidelines pertaining to Information

Appl. No: 10/606,753

Disclosure Statements have been complied with, and it is therefore respectfully requested that the Examiner consider the documents and make them of record.

Please charge any necessary fee or credit any overpayment in connection with this Information Disclosure Statement to Deposit Account No. 22-0261.

Respectfully submitted,

Date: October 29 2003 —



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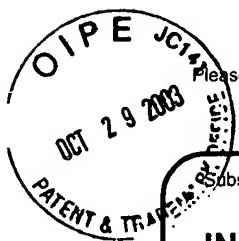
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

### Complete if Known

Application Number	10/606,753
Filing Date	June 27, 2003
First Named Inventor	Robert B. MAGILL et al.
Group Art Unit	2661
Examiner Name	Unassigned
Attorney Docket Number	32172-188433

### OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	A1	CHUANG et al., "Matching Output Queueing With A Combined Input/Output-Queued Switch," IEEE Journal on Selected Areas In Communications, Vol. 17, No. 6, pp. 1030-1039, (June, 1999).	
	A2	STOICA et al., "Exact Emulation of an Output Queueing Switch By a Combined Input Output Queueing Switch," presented at 6 <sup>th</sup> IEEE/IFIP IWQoS, pp. 218-224, (1998).	
	A3	ROJAS-CESSA et al., "CIXB-1: Combined Input-One-Cell Crosspoint Buffered Switch," Proc. 2001 IEEE Workshop on High Performance Switching and Routing, pp. 324-329, (May, 2001).	
	A4	ROJAS-CESSA et al., "CIXOB-k: Combined Input-Crosspoint-Output Buffered Packet Switch," Proc. IEEE Globecom 2001, pp. 2654-2660, (December, 2001).	
	A5	ELHANAY et al., "A Contention-Free Tbit/s Packet-Switching Architecture For ATM Over WDM Networks," IEICE Trans. Commun., Vol. E83-B, No. 2, pp. 225-230, (Feb. 2000)	
	A6	CHAO et al., "Delay-Bound Guarantee in Combined Input-Output Buffered Switches," Proc. IEEE GLOBECOM 2000, pp. 515-524, (November, 2000).	

Examiner  
Signature

Date  
Considered

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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